# Implementation of Start-Time Fair Queuing Algorithm in OPNET

CMPT885/ENSC835 Final Project

by

Daryn Mitchell <u>daryn@cs.sfu.ca</u>

Jack Man Shun Yeung yeung@cs.sfu.ca

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# Road map

- Background and Introduction to Start-Time Fair Queuing (SFQ)
- Project Implementation
- Some Simulation Results
- Conclusion
- References

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### Queuing in Routers - FIFO

- Buffers in routers allow multiple sources share one outgoing link
- Router schedules order in which packets leave
- Almost all FIFO/FCFS queuing (First-In, First-Out / First-Come First-Serve)
  - Simple, Cheap
  - Yet: Unfair?

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## Queuing in Routers – FIFO unfair

- No bandwidth guarantee in the presence of congestion
- Misbehaving sources can steal bandwidth from behaving sources



# Fair Queuing (FQ)

- Concept: Everyone gets a fair share of router bandwidth [Nagle RFC970, 1985]
- Assign a 'weight' to each data flow or category



## Start-Time Fair Queuing (SFQ)

- Proposed in 1996 by Goyal, Vin and Cheng [Goyal96]
- Like most FQ, assign weights to flows to give minimum bandwidth and maximum delay guarantees.
- Simple algorithm (unlike WFQ)
- Fair for VBR (unlike most FQ algrothms)
- Fair use of idle bandwidth (unlike Virtual Clock)

## SFQ Overview

#### Basic idea:

- Choose packet to send by lowest "start tag" among all flows
- Start Tags in a flow Packet Size
  generally increment by: Flow Service Rate
  - If packets arrive as agreed, they are all served with guaranteed maximum and average delay
  - If packets arrive faster than agreed, they will be delayed so they don't affect other flows

### **Project Proposal**

- Implement SFQ in OPNET as a queue module
- Create network incorporating SFQ in a router
- Run simulations to compare performance with other scheduling algorithms (e.g. WFQ and Virtual Clock)
- If possible, test with a real traffic trace

#### Implementation: Queue Process Model



SFQ Process Model Was built by modifying the Opnet "acb\_fifo" queue model to run according to the Start-Time Fair Queueing algorithm

#### Implementation: ICI

#### Opnet "Interface Control Information" Use to associate data fields with packets

- SFQ fields:
  - Start Tag
  - Finish Tag

- VC fields
  - Virtual clock

Auxiliary Virtual Clock

| - ICI Editor: sfq - 🗌                                     |         |               |              |
|---|---------|---------------|--------------|
| <u>F</u> ile <u>E</u> dit <u>W</u> indows                 |         |               | <u>H</u> elp |
| Attribute Name  | Туре    | Default Value |              |
| start_tag   | double  | 0             |              |
| finish_tag  | double  | 0             |              |
| Arrival time in queue                                     | double  | 0             |              |
| incoming_flow   | integer |               |              |
|   |         |               |              |
| Opened File: (/cs/grad2/yeung/op_project/model2/sfq.ic.m) |         |               |              |
| Start-Time Fair Oueuing                                   |         |               |              |



#### Implementation: Network Model



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#### Result 1: FIFO unfair

#### Incoming traffic per flow



#### Result 1: FIFO unfair

#### Throughput per flow



### Result 2: Verification of Fairness



- Source traffic as in VC paper [Alborz01]
  - src1: CBR 4 pk/s
  - src2:misbehaves
    - Fair queues make it wait
  - src3: VBR, avg. 4 pk/s
- Congestion

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### Result 2: Verification of Fairness

#### Throughput per flow



### Result 3: Fair Use of Idle Bandwidth



Input Traffic per flow

2 clients

- Client 0 (4 pks/sec) has a period of low arrival rate
- Client 1 (4 pks/sec) has a period of high arrival rate at same time
- There is congestion (8 pks/sec sharing 6 pks/sec)

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### Result 3: Fair Use of Idle Bandwidth



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## Conclusion: Project Proposal - Revisited

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- Createchetwookkinocoppostitigs56Qinacoutter
- Ran simulations to compare performance with other scheduling algorithms (EIGOWARQ Windual Voltate) Clock)
- Ifspedsaibee, Iteatfiveithaæer (Sitara Wiærtra/deEG)
- Converted/Implemented Virtual Clock as a queue module in Opnet

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